

# Light-Weight RefineNet for Real-Time Semantic Segmentation

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## Abstract

We consider an important task of effective and efficient semantic image segmentation. In particular, we adapt a powerful semantic segmentation architecture, called *RefineNet* [46], into the more compact one, suitable even for tasks requiring real-time performance on high-resolution inputs. To this end, we identify computationally expensive blocks in the original setup, and propose two modifications aimed to decrease the number of parameters and floating point operations. By doing that, we achieve more than twofold model reduction, while keeping the performance levels almost intact. Our fastest model undergoes a significant speed-up boost from 20 FPS to 55 FPS on a generic GPU card on  $512 \times 512$  inputs with solid 81.1% mean iou performance on the test set of PASCAL VOC [48], while our slowest model with 32 FPS (from original 17 FPS) shows 82.7% mean iou on the same dataset. Alternatively, we showcase that our approach is easily mixable with light-weight classification networks: we attain 79.2% mean iou on PASCAL VOC using a model that contains only 3.3M parameters and performs only 9.3B floating point operations.

## 1 Introduction

The number of empirical successes of deep learning keeps steadily increasing, and new technological and architectural breakthroughs become available practically every month. In particular, deep learning has become the default choice in most areas of computer vision, natural language processing, robotics, audio processing [21, 22, 24, 37, 40, 44, 57, 61, 63, 67, 69, 71, 72, 73]. Nevertheless, these breakthroughs often come at a price of expensive computational requirements, which hinders their direct applicability in tasks requiring real-time processing. The good part of this story is that it has been empirically shown by multiple researchers that oftentimes there are lots of redundant operations [1, 16, 17, 27, 31, 36, 58], and this redundancy can be (and should be) exploited in order to gain speed benefits while keeping performance mostly intact. The bad part of the story is that there are not (yet) general approaches on how to exploit such redundancies in most cases. For example, such methods as knowledge distillation [1, 3, 31, 58] and pruning [26, 27, 29, 42] require us to have an access to an already pre-trained large model in order to train a smaller model of the same (or near) performance. Obviously, there might be times when this is not feasible. In contrast, designing a novel architecture for a specific scenario of high-resolution inputs [64, 76] limits

the applicability of the same architecture on datasets with drastically different properties and often requires expensive training from scratch.

Here we are restricting ourselves to the task of semantic segmentation, which has proven to be pivotal on the way of solving scene understanding, and has been successfully exploited in multiple real-world applications, such as medical image segmentation [10, 59], road scene understanding [2, 74], aerial segmentation [58, 51]. These applications tend to rely on real-time processing with high-resolution inputs, which is the Achilles' heel of most modern semantic segmentation networks.

Motivated by the aforementioned observations we aim to tackle the task of real-time semantic segmentation in a different way. In particular, we build our approach upon *RefineNet* [49], a powerful semantic segmentation architecture that can be seamlessly used with any backbone network, such as ResNet [30], DenseNet [33], NASNet [82], or any other. This architecture belongs to the family of the 'encoder-decoder' segmentation networks [2, 53, 54], where the input image is first progressively downsampled, and later progressively upsampled in order to recover the original input size. We have chosen this particular architecture as it i) shows the best performance among the 'encoder-decoder' approaches, and ii) does not operate over large feature maps in the last layers as methods exploiting atrous convolution do [2, 5, 7, 74, 77]. The processing of large feature maps significantly hinders real-time performance of such architectures as DeepLab [2, 5, 7] and PSPNet [77] due to the increase in the number of floating point operations. As a weak point, the 'encoder-decoder' approaches tend to have a larger number of parameters due to the decoder part that recovers the high resolution output. In this work, we tackle the real-time performance problem by specifically concentrating on the decoder and empirically showing that both the number of parameters and floating point operations can be drastically reduced without a significant drop in accuracy.

In particular, i) we outline important engineering ameliorations that save the number of parameters by more than 50% in the original RefineNet; then ii) we pinpoint the redundancy of residual blocks in the architecture and show that the performance stays intact with those blocks being removed. We conduct extensive experiments on three competitive benchmarks for semantic segmentation - PASCAL VOC [18], NYUDv2 [62] and PASCAL Person-Part [6, 9], and with five different backbone networks - ResNet-50, ResNet-101, ResNet-152 [30], along with recently released NASNet-Mobile [82], and MobileNet-v2 [60]. The first three backbones are used for the direct comparison between our approach and the original RefineNet, while the last two are used to showcase that our method is orthogonal to the backbone compression, and that we can further benefit from combining these methods.

Quantitatively, our fastest ResNet model achieves 55 FPS on inputs of size  $512 \times 512$  on GTX 1080Ti, while having 81.1% mean intersection over union (mean iou) performance on PASCAL VOC, 41.7% mean iou on NYUDv2, and 64.9% mean iou on Person-Part. In contrast, our best performing model with 82.7% mean iou on VOC, 44.4% mean iou on NYUDv2, and 67.6% mean iou on Person-Part, still maintains solid 32 FPS.

All the models will be made publicly available to facilitate the usage of effective and efficient semantic segmentation in a multitude of applications.

## 2 Related work

**Semantic segmentation.** Early approaches in semantic segmentation relied on handcrafted features, such as HOG [14] and SIFT [50], in combination with plain classifiers [13, 70, 62, 53] and hierarchical graphical models [59, 41, 56]. This lasted until the resurgence of

deep learning, and a pivotal work by Long *et al.* [49], in which the authors converted a deep image classification network into a fully convolutional one, able to operate on inputs of different sizes. Further, this line of work was extended by using dilated convolutions and probabilistic graphical models [9, 45, 48, 72, 78]. Other works concentrated around the encoder-decoder paradigm [46, 53, 54], where the image is first progressively downsampled, and then progressively upsampled in order to generate the segmentation mask of the same size as the input. Most recently, Lin *et al.* [46], proposed RefineNet that extended the encoder-decoder approach by adding residual units inside the skip-connections between encoder and decoder. Zhao *et al.* [77] appended a multi-scale pooling layer to improve the information flow between local and global (contextual) information; Fu *et al.* [49] exploited multiple encoder-decoder blocks arranged in an hour glass manner [73] and achieved competitive results using DenseNet [53]. The current state-of-the art on the popular benchmark dataset PASCAL VOC [18] belongs to DeepLab-v3 [7] (86.9% mean intersection-over-union on the test set) and DeepLab-v3+ [8] (89.0%) based on ResNet [30] and Xception [10], correspondingly, where the authors included batch normalisation [59] layers inside the atrous spatial pyramid pooling block (ASPP) [9], made use of a large multi-label dataset [56], and added decoder in the latest version.

**Real-Time Segmentation.** Most methods outlined above either suffer from a large number of parameters [46], a large number of floating point operations [9, 8, 7, 77], or both [49, 53, 54, 73]. These issues constitute a significant drawback of exploiting such models in applications requiring real-time processing.

To alleviate them, there have been several task-specific approaches, e.g., ICNet [76], where the authors adapt PSPNet [77] to deal with multiple image scales progressively. They attain the speed of 30 FPS on  $1024 \times 2048$  images, and 67% mean iou on the validation set of CityScapes [12], but it is not clear whether this approach would still acquire solid performance on other datasets with low-resolution inputs. Alternatively, Li *et al.* [43] propose a cascading approach where in each progressive level of the cascade only a certain portion of pixels is being processed by explicitly setting a hard threshold on intermediate classifier outputs. They demonstrate performance of 14.3 FPS on  $512 \times 512$  input resolution with 78.2% mean iou on PASCAL VOC [18]. We also note that several other real-time segmentation networks have been following the encoder-decoder paradigm, but have not been able to acquire decent performance levels. Concretely, SegNet [7] achieved 40 FPS on inputs of size  $360 \times 480$  with only 57.0% mean iou on CityScapes, while ENet [54] were able to perform inference of 20 FPS on inputs of size  $1920 \times 1080$  with 58.3% mean iou on CityScapes.

**Real-Time Inference in Other Domains.** Multiple task-agnostic solutions have been proposed to speed-up inference in neural networks by compression. Current methods achieve more than  $100\times$  compression rates [52]. Among them, most popular approaches include quantisation [23, 79, 81], pruning [26, 27, 29, 42] and knowledge distillation [11, 9, 31, 58]. Other notable examples are centered around the idea of low-rank factorisation and decomposition [17, 36], where a large layer can be decomposed into smaller ones by exploiting (linear) structure.

While all the methods above have proven to be effective in achieving significant compression rates, an initial powerful, but large pre-trained model for the task at hand must be acquired first. We further note that these methods can be applicable as a post-processing step on top of our approach, but we leave this direction for later exploration.

Finally, most recently, a multitude of new, light-weight architectures [32, 34] has been

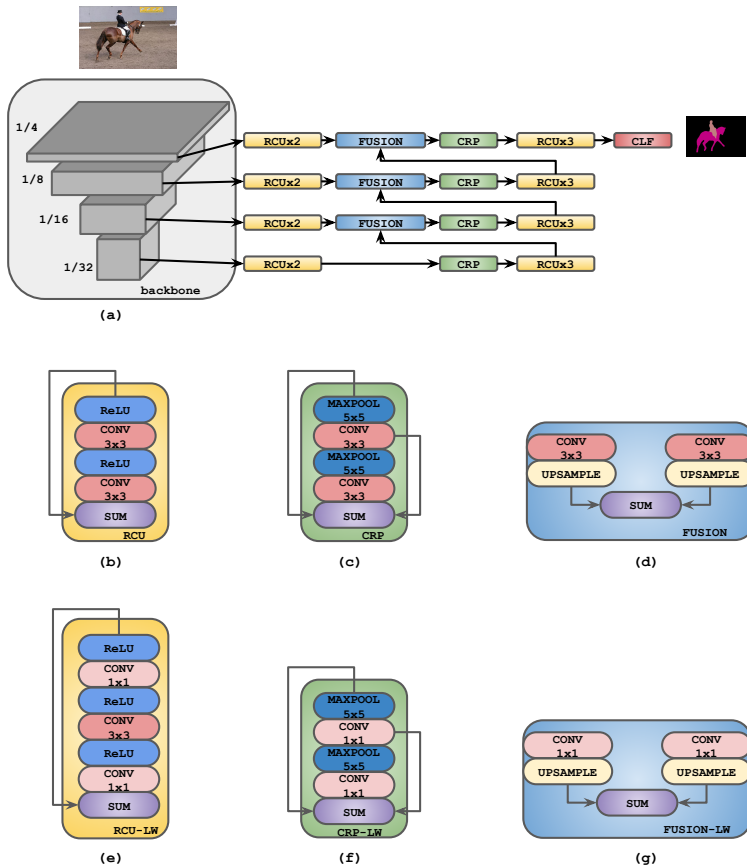


Figure 1: RefineNet structure. (a) General network architecture with RefineNet for semantic segmentation, where *CLF* stands for a single  $3 \times 3$  convolutional layer with the number of channels being equal to the number of output classes; (b)-(d) general outline of original RCU, CRP and fusion blocks; (e)-(g) light-weight RCU, CRP and fusion blocks. In the interests of brevity, we only visualise 2 convolutional layers for the CRP blocks (instead of 4 used in the original architecture). Note that we do not use any RCU blocks in our final architecture as discussed in Sec. 3.3.

proposed, and it has empirically been shown that models with a smaller number of parameters are able to attain solid performance. To highlight that our approach is orthogonal to such advances in the design of classification networks, we conduct experiments with two light-weight architectures, namely, NASNet-Mobile [24], and MobileNet-v2 [60], and show competitive results with very few parameters and floating point operations.

### 3 Light-Weight RefineNet

Here we will outline our approach aimed to decrease the number of parameters and floating point operations of the original RefineNet architecture, while keeping the performance levels the same.

We start by describing the original setup, and then continue with the explanation and discussion of the changes that we propose to be made.

### 3.1 RefineNet Primer

As mentioned above, the RefineNet architecture belongs to the family of the encoder-decoder approaches. As the encoder backbone, it can re-use most popular classification networks; in particular, the original contribution was showcased using residual networks [30]. The approach does not amend an underlying classification network in any way except for omitting last classification layers.

In the decoder part, RefineNet relies on two types of additional abstractions: residual convolutional unit (RCU) (Fig. 1(b)) and chained residual pooling (CRP) (Fig. 1(c)). The first one is a simplification of the original residual block [30] without batch normalisation [35], while the second one is a sequence of multiple convolutional and pooling layers, also arranged in a residual manner. All of these blocks use  $3 \times 3$  convolutions and  $5 \times 5$  pooling with appropriate padding so that the spatial size would stay intact.

The decoding process starts with the propagation of the last output from the classifier (with the lowest resolution) through two RCU blocks followed by four pooling blocks of CRP and another three RCU blocks before being fed into a fusion block (Fig. 1(d)) along with the second to last feature map. Inside the fusion block, each path is convolved with  $3 \times 3$  convolution and upsampled to the largest resolution among the paths. Two paths are then summed up, and analogously further propagated through several RCU, CRP and fusion blocks until the desired resolution is reached. The final convolutional layer produces the score map.

### 3.2 Replacing 3x3 convolutions

We start by highlighting that the most expensive parts in terms of both the number of parameters and the number of floating point operations of the original RefineNet stem from the ubiquitous usage of  $3 \times 3$  convolutions. Thus, we focus on replacing them with simpler counterparts without performance drops.

Intuitively, convolutions with larger kernel sizes aim to increase the receptive field size (and the global context coverage), while  $1 \times 1$  convolutions are merely transforming per-pixel features from one space to another locally. We argue that in the case of RefineNet we do not require expensive  $3 \times 3$  convolutions at all, and we are able to show empirically that replacing them with  $1 \times 1$  convolutions does not hurt performance. Furthermore, we evaluate the empirical receptive field size [36] for both cases, and do not find any significant difference (Section 5.1). In particular, we replace  $3 \times 3$  convolutions within CRP and fusion blocks with  $1 \times 1$  counterpart (Fig. 1(f-g)), and we amend RCU into the one with the bottleneck design [30] (Fig. 1(e)). This way, we reduce the number of parameters by more than  $2 \times$  times, and the number of FLOPs by more than  $3 \times$  times (Table 1).

Alternatively, we might have reverted to recently proposed depthwise separable convolutions [14] that are proven to save lots of parameters without sacrificing performance, but our approach is even simpler as it does not use any  $3 \times 3$  convolutions at all, except for the last classification layer.

### 3.3 Omitting RCU blocks

We proceeded with initial experiments and trained the light weight architecture outlined in the previous section using PASCAL VOC [13]. We were able to achieve close to the original network performance, and, furthermore, observed that removing RCU blocks did not lead

Model	Parameters,M	FLOPs,B
RefineNet-101 [47]	118	263
<b>RefineNet-101-LW-WITH-RCU</b>	54	76
<b>RefineNet-101-LW</b>	<b>46</b>	<b>52</b>

Table 1: Comparison of the number of parameters and floating point operations on  $512 \times 512$  inputs between original RefineNet, Light-Weight RefineNet with RCU (**LW-WITH-RCU**), and Light-Weight RefineNet without RCU (**LW**). All the networks exploit ResNet-101 as backbone.

to any accuracy deterioration, and, in fact, the weights in RCU blocks almost completely saturated.

To confirm that this only occurs in the light weight case, we explored dropping RCU blocks in the original RefineNet architecture, and we experienced more than 5% performance drop on the same dataset. We argue that this happens due to the RCU blocks being redundant in the  $1 \times 1$  convolution regime, as the only important goal of increasing the contextual coverage is essentially performed by pooling layers inside CRP. To back up this claim, we conduct a series of ablation experiments which are covered later in Section 5.2.

Our final architecture does not contain any RCU blocks and only relies on CRP blocks with  $1 \times 1$  convolutions and  $5 \times 5$  max-pooling inside, which makes our method extremely fast and light-weight.

### 3.4 Adaptation to different backbones

A significant practical benefit of the RefineNet architecture is that it can be mixed with any backbone network as long as the backbone has several subsampling operations inside (which is the case for most SOTA classifiers). Thus, there are no specific changes needed to be made in order to apply the RefineNet architecture using any other model. In particular, in the experiments section, we showcase its adaptation using efficient NASNet-Mobile [82] and MobileNet-v2 [60] networks; we still achieve solid performance with the limited number of parameters and floating point operations.

## 4 Experiments

In the experimental part, our aims are to prove empirically that we are able i) to achieve similar performance levels with the original RefineNet while ii) significantly reducing the number of parameters and iii) drastically increasing the speed of a forward pass; and iv) to highlight the possibility of applying our method using other architectures.

To this end, we consider three segmentation datasets, namely, NYUDv2 [64], PASCAL VOC [18] and PASCAL Person-Part dataset [6, 9], and five classification networks, i.e., ResNet-50, ResNet-101, ResNet-152 [60], NASNet-Mobile [82] and MobileNet-v2 [60] (only for Pascal VOC), all of which have been pre-trained on ImageNet [15]. As a general practice, we report mean intersection over union [18] on each benchmark. Additional results on PASCAL Context [52] and CityScapes [12] are given in the supplementary material.

We perform all our experiments in PyTorch [53], and train using stochastic gradient descent with momentum. For all residual networks we start with the initial learning rate of  $5e-4$ , for NASNet-mobile and MobileNet-v2 we start with the learning rate of  $1e-3$ . We keep batch norm statistics frozen during the training.

Model	NYUD mIoU,%	Person mIoU,%	Params,M	Runtime,ms
FCN16-s RGB-HHA [49]	34.0	-	-	-
Context [45]	40.6	-	-	-
DeepLab-v2-CRF [6]	-	64.9 ( <i>msc</i> )	<b>44</b>	-
RefineNet-50 [46]	42.5	65.7	99	54.18 ± 0.46
RefineNet-101 [46]	43.6	67.6	118	60.25 ± 0.53
RefineNet-152 [46]	<b>46.5</b> ( <i>msc</i> )	<b>68.8</b> ( <i>msc</i> )	134	69.37 ± 0.78
<b>RefineNet-LW-50</b> (ours)	41.7	64.9	<b>27</b>	<b>19.56</b> ± 0.29
<b>RefineNet-LW-101</b> (ours)	43.6	66.7	<b>46</b>	<b>27.16</b> ± 0.19
<b>RefineNet-LW-152</b> (ours)	44.4	67.6	<b>62</b>	<b>35.82</b> ± 0.23

Table 2: Quantitative results on the test sets of NYUDv2 and PASCAL Person-Part. Mean iou, the number of parameters and the runtime (mean±std) of one forward pass on  $625 \times 468$  inputs are reported, where possible. Multi-scale evaluation is defined as *msc*.

For benchmarking, we use a workstation with 8GB RAM, Intel i5-7600 processor, and one GT1080Ti GPU card under CUDA 9.0 and CuDNN 7.0. For fair comparison of runtime, we re-implement original RefineNet in PyTorch. We compute 100 forward passes with random inputs, and average the results; when reporting, we provide both the mean and standard deviation values.

**NYUDv2.** We first conduct a series of initial experiments on NYUDv2 dataset [25, 64]. This dataset comprises 1449 RGB-D images with 40 segmented class labels, of which 795 are used for training and 654 for testing, respectively. We do not make use of depth information in any way. We reduce the learning rate by half after 100 and 200 epochs, and keep training until 300 epochs, or until earlier convergence.

Our quantitative results are provided in Table 2 along with the results from the original RefineNet, and other competitive methods on this dataset. We note that for all ResNet networks we are able to closely match the performance of the original RefineNet, while having only a slight portion of the original parameters. This further leads to a significant twofold speedup.

**Person-Part.** PASCAL Person-Part dataset [8, 9] consists of 1716 training and 1817 validation images with 6 semantic classes, including head, torso, and upper/lower legs/arms, plus background. We follow the same training strategy as for NYUDv2, and provide our results in Table 2. Again, we achieve analogous to the original models results.

**PASCAL VOC.** We continue our experiments with a standard benchmark dataset for semantic segmentation, PASCAL VOC [18]. This dataset consists of 4369 images with 20 semantic classes plus background, of which 1464 constitute the training set, 1449 - validation, and 1456 - test, respectively. As commonly done, we augment it with additionally annotated VOC images from BSD [28], as well as images from MS COCO [47].

We train all the networks using the same strategy, but with different learning rates as outlined above: in particular, we reduce the learning rate by half after 20 epochs of COCO training and after 50 epochs of the BSD training, and keep training until 200 total epochs, or until the accuracy on the validation set stops improving.

Our quantitative results on validation and test sets are given in Table 3 along with the results from the original RefineNet. We again notice that for all ResNet networks, we achieve performance on-par with the original RefineNet; for NASNet-Mobile and MobileNet-v2, we



Model	val mIoU,%	test mIoU,%	FLOPS,B
DeepLab-v2-ResNet-101-CRF [5]	77.7	79.7	-
RefineNet-101 [46]	-	82.4	263
RefineNet-152 [46]	-	<b>83.4</b>	283
<b>RefineNet-LW-50</b> (ours)	78.5	81.1 <sup>1</sup>	<b>33</b>
<b>RefineNet-LW-101</b> (ours)	80.3	82.0 <sup>2</sup>	52
<b>RefineNet-LW-152</b> (ours)	82.1	82.7 <sup>3</sup>	71
MobileNet-v1-DeepLab-v3 [50]	75.3	-	14.2
MobileNet-v2-DeepLab-v3 [50]	75.7	-	<b>5.8</b>
<b>RefineNet-LW-MobileNet-v2</b> (ours)	<b>76.2</b>	<b>79.2</b> <sup>4</sup>	<b>9.3</b>
<b>RefineNet-LW-NASNet-Mobile</b> (ours)	<b>77.4</b>	<b>79.3</b> <sup>5</sup>	11.4

Table 3: Quantitative results on PASCAL VOC. Mean iou and the number of FLOPs on  $512 \times 512$  inputs are reported, where possible.



Figure 2: Visual results on validation set of PASCAL VOC with residual models (RF), MobileNet-v2 (MOB) and NASNet-Mobile (NAS). The original RefineNet-101 (RF-101) is our re-implementation.

outperform both MobileNet-v1+DeepLab-v3 and MobileNet-v2+DeepLab-v3 approaches [50], which are closely related to our method. Qualitative results are provided on Figure 2.

## 5 Discussion

As noted above, we are able to achieve similar results by omitting more than half of the original parameters. This raises the question on how and why this does happen. We conduct a series of experiments aimed to provide some insights into this matter. More details are provided in the supplementary material.

<sup>1</sup><http://host.robots.ox.ac.uk:8080/anonymous/ZAC8JH.html>

<sup>2</sup><http://host.robots.ox.ac.uk:8080/anonymous/RBAJXC.html>

<sup>3</sup><http://host.robots.ox.ac.uk:8080/anonymous/EFA17T.html>

<sup>4</sup><http://host.robots.ox.ac.uk:8080/anonymous/90TQVN.html>

<sup>5</sup><http://host.robots.ox.ac.uk:8080/anonymous/WRYPBD.html>



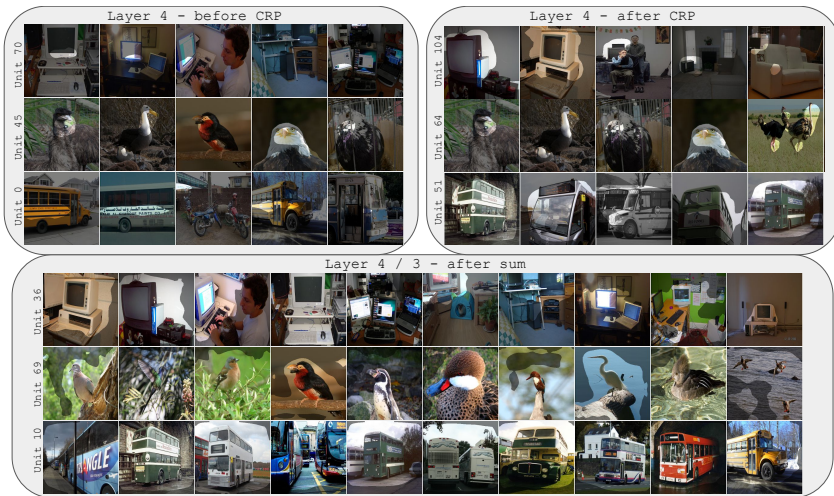


Figure 3: Comparison of empirical receptive field before CRP (top left), after CRP (top right), and after the first summation of levels 4 and 3 (bottom) in RefineNet-LW-101 pre-trained on PASCAL VOC. Top activated images along with top activated regions are shown.

## 5.1 Receptive field size

First, we consider the issue of the receptive field size. Intuitively, dropping  $3 \times 3$  convolutions should significantly harm the receptive field size of the original architecture. Nevertheless, we note that we do not experience this due to i) the skip-design structure of RefineNet, where low-level features are being summed up with the high-level ones, and ii) keeping CRP blocks that are responsible for gathering contextual information.

In particular, we explore the empirical receptive field (ERF) size [80] in Light-Weight RefineNet-101 pre-trained on PASCAL VOC. Before CRP ERF is concentrated around small object parts, and barely covers the objects (Fig. 3). The CRP block tends to enlarge ERF, while the summation with the lower layer features further produces significantly larger activation contours.

## 5.2 Representational power

Even though the RCU block seems not to influence the receptive field size of the network, it might still be responsible for producing important features for both segmentation and classification.

To evaluate whether it is the case indeed, we conduct ablation experiments by adding multi-label classification and segmentation heads in the lowest resolution block of RefineNet-101 pre-trained on PASCAL VOC 1) before RCU, 2) after RCU, and 3) after CRP. We fix the rest of the trained network and fine-tune only these heads separately using 1464 training images of the VOC dataset. We evaluate all the results on the validation subset of VOC.

As seen from Table 4, CRP is the main driving force behind accurate segmentation and classification, while the RCU block improves the results just marginally.

Model	mIoU,%	acc,%
Before RCU	54.63	95.39
After RCU	55.16	95.42
After CRP	<b>57.83</b>	<b>97.71</b>

Table 4: Ablation experiments comparing CRP and RCU. Multi-label accuracy (without background) and mean IoU are reported as measured on the validation set of PASCAL VOC.

## 6 Conclusions

In this work, we tackled the problem of rethinking an existing semantic segmentation architecture into the one suitable for real-time performance, while keeping the performance levels mostly intact. We achieved that by proposing simple modifications to the existing network and highlighting which building blocks were redundant for the final result. Our method can be applied along with any classification network for any dataset and can further benefit from using light-weight backbone networks, and other compression approaches. Quantitatively, we were able to closely match the performance of the original network while significantly surpassing its runtime and even acquiring 55 FPS on  $512 \times 512$  inputs (from initial 20 FPS). Besides that, we demonstrate that having convolutions with large kernel sizes can be unnecessary in the decoder part of segmentation networks, and we will devote future work to further cover this topic.

**Acknowledgements** The authors would like to thank the anonymous reviewers for their helpful and constructive comments. This research was supported by the Australian Research Council through the Australian Centre for Robotic Vision (CE140100016), the ARC Laureate Fellowship FL130100102 to IR, and the HPC cluster Phoenix at the University of Adelaide.

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