Evolutionary Synthesis of Nand Logic: Dissecting a Digital Organism

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Abstract—According to conservation of information theorems, performance of an arbitrarily chosen search, on average, does no better than blind search. Domain expertise and prior knowledge about search space structure or target location is therefore essential in crafting the search algorithm. The effectiveness of a given algorithm can be measured by the active information introduced to the search. We illustrate this by identifying sources of active information in Avida, a software program designed to search for logic functions using nand gates. Avida uses stair step active information by rewarding logic functions using a smaller number of nands to construct functions requiring more. Removing stair steps deteriorates Avida’s performance while removing deleterious instructions improves it. Some search algorithms use prior knowledge better than others. For the Avida digital organism, a simple evolutionary strategy generates the Avida target in far fewer instructions using only the prior knowledge available to Avida.

Index Terms—conservation of information, active information, assisted search, evolutionary search, endogenous information, importance sampling, sea of gates, nand logic, no free lunch theorems.

I. INTRODUCTION

Even moderately sized search problems require external assistance to be successful. The contribution of the external source to the search can be measured as active information [4], [5], [7]. Sources of active information include (a) an oracle that provides fitness information (or scores) for the search, (b) the initialization, properly chosen, can improve the probability of success, and (c) knowledge about the search space structure. If prior assumptions about the search are incorrect, the search can perform worse than a random search. Since every constructive choice of priors has a corresponding deleterious choice, the average performance over all searches is the same as a random search [3], [4], [5], [8], [15], [19], [20], [21], [22], [23], [27], [28], [29], [30]. This search property is dubbed conservation of information [4], [5], [7], [9], [23] as popularized by the no free lunch theorem [3], [8], [15], [30].

The open source Avida program [16] performs logic function synthesis using nand gates [11], [13], [25], [26], [32] and evolutionary search [18]. We show that, as a search algorithm, Avida generates active information from a number of knowledge sources provided by the programmer and, with respect to an evolutionary strategy, performs poorly with respect to other search strategies using the same prior knowledge.

A. Information Measures

To assess the performance of a search, we use the following information measures [4], [5], [7].

1) The endogenous information is a measure of the difficulty of a search and is given by

\[ I_\Omega = -\log_2 p \]  

where \( p \) is a reference probability of a successful assisted random search.

2) Let the probability of success of an assisted search under the same set of constraints be \( q \). Denote the exogenous information of a search program as

\[ I_S := -\log_2 q. \]

3) The difference between the endogenous and exogenous information is the active information.

\[ I_+ := I_\Omega - I_S = -\log_2 \frac{p}{q}. \]

If the knowledge about the space is not accurate or is otherwise misleading, the active information can be negative.

For a computer program generated by an alphabet of instructions as is the case with Avida, a resource constraint must be imposed. Otherwise, unlimited time and computer resources allows an exhaustive search. Let \( \mathcal{J} \) denote the instruction count and \( \mathcal{J}_m \) the maximum allowable number of instructions before abandoning the search. Under an instruction count constraint, \( \mathcal{J} = \{ \mathcal{J} \mid \mathcal{J} \leq \mathcal{J}_m \} \) the active information per instruction (AIPI) is

\[ I_\oplus := \mathbb{E} \left[ \frac{I_\Sigma}{\mathcal{J}_m} \right] \]  

where \( \mathbb{E} \) denotes expectation [17]. The AIPI can be estimated by averaging the active information per instruction over \( K \) trials of \( \mathcal{J} \) instructions or less. For the \( k^{th} \) simulation, there are two possibilities.

1) Success is achieved with \( \mathcal{J}_k \leq \mathcal{J}_m \) instructions in which case the point estimate of the AIPI is \( I_\Omega / \mathcal{J}_k \).

2) If a success is not achieved with \( \mathcal{J}_k = \mathcal{J}_m \) instructions, then the point estimates of the active information and the AIPI both have a value of zero.
Thus, with $\zeta_k = 1$ for a success in $\mathcal{J}$ or fewer instructions and $\zeta_k = 0$ and $\mathcal{J}_k = \mathcal{J}$ for a failure, we can estimate the AIPI as the harmonic average over $K$ trials.

$$I_{\mathcal{H}} \approx \frac{1}{K} \sum_{k=1}^{K} \zeta_k \left( \frac{I_{\Omega}}{\mathcal{J}_k} \right)$$

$$= \frac{I_{\Omega}}{K} \sum_{\text{successes}} \frac{1}{\mathcal{J}_k}. \quad (3)$$

The AIPI needs to be interpreted with the same caution as the average speed of an auto on a road trip. Instantaneous values can be significantly higher or lower than the average. We dub $I_{\mathcal{H}} / I_{\Omega}$ the normalized AIPI, or the NAIPI.

II. EVOLUTIONARY SEARCH USING NAND LOGIC

The information measures of many search algorithms are beyond direct analytic evaluation and require empirical analysis using, for example, Monte Carlo simulation. One is evolutionary search for synthesis of logic functions.

The nand gate is one of two logic functions from which all other logic can be synthesized [14]. The other is the nor gate. Chips containing a sea of gates [6], [11], [13], all nand, are therefore capable of universal logic. This property allows evolutionary development of logic functions using the nand gate as the single logic component [1], [12], [25], [26], [32].

Avida [16], illustrated in Figure 1, performs logic synthesis using only the nand gate. The motivation behind Avida is not engineering design, but is rather to [16]

“... show how complex [biological] functions can originate by random mutation and natural selection.”

Avida generates an output equal to a logic combination of the inputs, X and Y. The logic operation under consideration that requires the most nand gates (five) is the XNOR. This is the ultimate goal of the search. The example of Avida in Figure 1 has performed the XNOR. The first bits X and Y are 1 and 0 and the XNOR of 1 and 0 is 0. This then is the first bit of the output string. The second bits of X and Y are both ones and the XNOR of two ones is 1. This is the second bit in the output. Continuing with subsequent bits, we say the machine in Figure 1 generates an XNOR if the bitwise XNOR’s of X and Y is equal to the corresponding output bit. Since the XNOR is 1 if both input bits are the same and 0 if they are not, Avida refers to the XNOR as an EQU [16].

Avida uses a small alphabet of instructions (see Table I) to perform a specified function like XNOR (EQU) is similar to generating a specified word or phrase from the English alphabet somewhere within an alphabet of characters. The Avida simulation actually makes use of three inputs, X, Y and Z, in a circular queue. Whenever an input is read by the program, it is removed from the queue and then placed at the back of the queue. As a result, three consecutive reads will read in three different inputs, whereas the fourth will read the first input in again. This property was not addressed in the original paper.

BX, and CX, on which to perform the operations. There is also a stack that can pop a stored 32 bit word into AX, BX and CX. An element from AX, BX and CX can likewise be pushed onto the top of the stack. The goal is to choose instructions from Table I so that the output, written by operation (q) $\mathrm{Σ}_0$, is the bitwise XNOR of the input registers X and Y. For most generations of the evolutionary search, Avida uses 3600 of the digital organisms in Figure 1.

A. Problem Difficulty

Like choosing letters from the alphabet to form a sequence of words that will pass a spell check, the goal of Avida is to search for a sequence of instructions that has meaning with respect to the logic functions in Table II. The question is - how difficult is it to generate the logic functions in Table II using the $N = 26$ instructions in Table I in a sequence of instructions?

1) Instruction Count: We want to evaluate the relative performance between Avida and competing algorithms. The most straightforward measure of computational cost is measurement of the actual CPU time of the process. CPU time, however, can differ considerably on different hardware. Other external factors, such as user interaction with the system, may also result in a non-accurate measure of the computational cost.

Another measure of the cost in search algorithms is a query count. In Avida, however, the computational demands of a
TABLE I

<table>
<thead>
<tr>
<th>#</th>
<th>Operation</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>nop-A</td>
<td>No-Operation. May modify the operation of the instruction before it.</td>
</tr>
<tr>
<td>2</td>
<td>nop-B</td>
<td>No-Operation. May modify the operation of the instruction before it.</td>
</tr>
<tr>
<td>3</td>
<td>nop-C</td>
<td>No-Operation. May modify the operation of the instruction before it.</td>
</tr>
<tr>
<td>4</td>
<td>if-n-equal</td>
<td>Compares two values, skips the next instruction if they are equal.</td>
</tr>
<tr>
<td>5</td>
<td>if-less</td>
<td>Compares two values, skips the next instruction if the first is less.</td>
</tr>
<tr>
<td>6</td>
<td>push</td>
<td>Puts a copy of the value in the target register onto the top of the stack.</td>
</tr>
<tr>
<td>7</td>
<td>pop</td>
<td>Removes a value from the top of the stack and places it into the target register.</td>
</tr>
<tr>
<td>8</td>
<td>swap-stk</td>
<td>Switches the stack between two available stacks.</td>
</tr>
<tr>
<td>9</td>
<td>swap</td>
<td>Swaps the value of the target register with the next register.</td>
</tr>
<tr>
<td>10</td>
<td>shift-r</td>
<td>Shift the bits of the value in the target register to the right. This is the same as dividing by two and rounding down.</td>
</tr>
<tr>
<td>11</td>
<td>shift-l</td>
<td>Shift the bits of the value in the target register to the left. This is the same as multiplying by two.</td>
</tr>
<tr>
<td>12</td>
<td>inc</td>
<td>Increments the value in the target register by one.</td>
</tr>
<tr>
<td>13</td>
<td>dec</td>
<td>Decrements the value in the target register by one.</td>
</tr>
<tr>
<td>14</td>
<td>add</td>
<td>Adds the values of the BX and CX register, placing the result in the target register.</td>
</tr>
<tr>
<td>15</td>
<td>sub</td>
<td>Subtracts the value of CX from BX, placing the result in the target register.</td>
</tr>
<tr>
<td>16</td>
<td>nand</td>
<td>Bitwise-nands the values of BX and CX together, placing the result in the target register.</td>
</tr>
<tr>
<td>17</td>
<td>I0</td>
<td>Outputs the target register, checking if for any tasks completed. Reads the next input into the target register.</td>
</tr>
<tr>
<td>18</td>
<td>h-alloc</td>
<td>Allocates additional memory to be used for a daughter organism.</td>
</tr>
<tr>
<td>19</td>
<td>h-divide</td>
<td>Splits the daughter and the parent into separate organisms.</td>
</tr>
<tr>
<td>20</td>
<td>h-copy</td>
<td>Copies a single instruction from the read head to the write head.</td>
</tr>
<tr>
<td>21</td>
<td>h-search</td>
<td>Searches the genome for a pattern of nops, moving the flow-head to their location.</td>
</tr>
<tr>
<td>22</td>
<td>mov-head</td>
<td>Moves one of the other heads to the flow-head.</td>
</tr>
<tr>
<td>23</td>
<td>jmp-head</td>
<td>Causes one of the heads the jump forward by the value in the CX register.</td>
</tr>
<tr>
<td>24</td>
<td>get-head</td>
<td>Copies the position of one of the heads into the CX register.</td>
</tr>
<tr>
<td>25</td>
<td>if-label</td>
<td>Checks to see whether a certain pattern of nops has just been copied. If they have not, skips an instruction.</td>
</tr>
<tr>
<td>26</td>
<td>set-flow</td>
<td>Sets the position of the flow head to the value in target register.</td>
</tr>
</tbody>
</table>

query can vary widely.

A reasonable accounting metric for Avida is an instruction count. We define an instruction as the execution of any one of the entries in Table I.

2) A Single Avida Organism: Avida’s performance can be evaluated using Monte Carlo simulation. Here are some useful definitions.

- A program is a list of 100 instructions. 85 are chosen randomly and 15 are specified. The number of instructions in an executed program varies. The same instruction, for example, can be executed more than once.
- A query is a sequence of programs executed until either an EQU is found, or until it is a sequence of programs executed until either an EQU is found, or until

\[
\hat{J} = 10.8 \text{ billion instructions}
\] (4) are used. This number is roughly the number of instructions used by the Avida default.

- A program or query is diminished if instructions (v) mov-head and (w) jmp-head in Table I are not used. They nearly always cause the failure of an EQU calculation. A program or query including all of the instructions, including (v) mov-head and (w) jmp-head, is dubbed full.
- Let D denote diminished and F full. \(P_D\) and \(P_F\) denote diminished and full programs while \(Q_D\) and \(Q_F\) correspond to diminished and full queries. An S will denote success in finding an EQU, so \(P_{S|F}\) and \(Q_{S|F}\) denote successful full programs and queries, while \(P_{S|D}\) and \(Q_{S|D}\) are the diminished equivalents.

\(\hat{J}\) = 100,000 updates \& 3600 programs \& 300 instructions (on average) per program per update [16].

The instructions (v) mov-head and (w) jmp-head both manipulate various heads in the Avida CPU. There are three heads which can be manipulated by these instruction, the read-head, write-head, or the instruction pointer. The read and write heads are setup for the copying process at the beginning of an Avida program. If they are moved, the replication process will not work correctly. If the instruction pointer is changed the Avida program will jump to a different position in its program, in all probability causing it to loop in a particular portion of its program never reaching the copy loop.
3) An Estimate of the Endogenous Information of a Full Program: A total of \(|Q_D| = 1420\) diminished queries were performed and a total of \(|S| = 21\) EQU’s resulted. The average number of instructions per diminished program, \(P_D\), was

\[
\mathbb{E}[\text{per } P_D] \approx 637.9 \text{ instructions}
\]

so the total number of diminished programs simulated is

\[
|P_D| = \frac{\mathbb{E}[\text{per } P_D]}{\mathbb{E}[\text{per } P_F]} \approx 2.40 \times 10^{10}
\]

A separate run of full programs\(^4\) gave

\[
\mathbb{E}[\text{per } P_F] \approx 1972 \text{ instructions.}
\]

The maximum number of instructions allowed by Avida was 2000.

Of the \(N_F = 26^{85} = 1.87 \times 10^{120}\) possible full programs, there are over \(10^{108}\) that compute an EQU. To show this, consider first the number of diminished programs. \(N_D = 24^{85} = 2.08 \times 10^{117}\). The probability a diminished program will compute an EQU is about \(\text{Pr}[\text{EQU} | P_D] \approx \frac{|S|}{|P_D|} = 8.73, 5 \times 10^{-10}\). The number of diminished programs capable of computing EQU is therefore \(|P_{S|D}| = N_D \times \text{Pr}[\text{EQU} | P_D] \approx 1.82 \times 10^{108}\). If we assume all programs containing (v) mov-head or (w) jmp-head will fail, this is also the number of full programs that will compute EQU, i.e., \(|P_{S|F}| \approx |P_{S|D}|\). The probability of choosing a successful program randomly from the set of full programs is thus

\[
p = \text{Pr}[P_{S|F}] \approx \frac{|P_{S|D}|}{N_F} = 9.69 \times 10^{-13}.
\]

The endogenous information of the difficulty of generating an EQU with 85 randomly selected instructions from Table I follows from (1) as

\[
I_\Omega \approx 40 \text{ bits}
\]

4) Algorithm \(\Omega\), Repeated Diminished Programs: Running a program a repeated number of times generates more information than a single run [5]. For the \(|Q_D| = 1420\) diminished query simulations each to a maximum of \(\mathbb{E}[\] instructions each, the NAIPI, calculated using (3), was

\[
I_\beta / I_\Omega \approx 5.78 \times 10^{-12}
\]

This is the (small) NAIPI obtained from (a) repeated queries and (b) using only diminished programs.

5) Stair Step Active Information in the NAND Search: Knowledge that higher level nand logic can be built on lower level nand logic can be used to introduce stair step active information into the search for the EQU. Stair step active information, discussed in detail elsewhere [5], is an important source of active information in Avida.

Examples of synthesizing the nine logic functions in Table II using only nand gates is well known and is illustrated in minimal form\(^5\) in Figures 2 through 4. They are included here to show that the logic can build on itself. For example, Figure 2 logic function \#3 shows the AND as simply a cascade connection of the NOT circuit \#1 and the NAND function in \#2. Higher up the list, the \#9 XNOR (EQU) in Figure 4 is a simple cascade connection of the \#8 XOR circuit with the \#2 NAND. The \#8 XOR, in turn, can be synthesized using other lower numbered logic circuits. Not all of the steps need be directly useful to the subsequent target.

In the nand gate synthesis, logic functions with fewer gates are rewarded as possible steps towards achieving the ultimate EQU target at the top of the stairs. The rewarding in Avida is done using the Score column in Table II. The log\(_2\) of the Score is equal to the minimum number of gates required to perform the logic function. If two or more logic functions are generated by a digital organism, the fitness of the organism is the product of the scores. Each logic function is counted only once in this tally. For example, if an organism generates three separate cases of the NOT function, the fitness rule only counts them as one occurrence.

\[\text{Fig. 2. Minimal NAND logic. Logic function is synthesized using the minimum possible number of NAND gates. Continued in Figure 3.}\]

For all of the subsequent simulations, a resource bound of \(\mathbb{E}[\] in (4) is imposed.

1) Performance of Avida. Using 3600 digital organisms of

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\(^4\)10,272,633 full programs

\(^5\)Figures 2 through 4 show configurations using the fewest possible number of nand gates. Other configurations are possible.
The effects of removing stair steps from Avida (A) and the ratcheted evolutionary strategy searches (R) for the EQU. The estimate of NAIPI is computed using (3). The comparative numbers for repeated running of randomly generated diminished Avida programs, as described in Section II-A4, is in the last row. All simulations, except 2, have 353 runs. The run count for 2 is 1420 queries. The values in the $I_2/I_1$ column should be multiplied by $10^{-9}$.

<table>
<thead>
<tr>
<th>Model</th>
<th>Steps Removed</th>
<th>Successes</th>
<th>$I_2/I_1$</th>
</tr>
</thead>
<tbody>
<tr>
<td>$A_1$</td>
<td>None (All Steps Enabled)</td>
<td>346</td>
<td>1.90</td>
</tr>
<tr>
<td>$A_2$</td>
<td>XOR/NOR</td>
<td>319</td>
<td>1.37</td>
</tr>
<tr>
<td>$A_3$</td>
<td>XOR/NOR/OR/AND_N</td>
<td>227</td>
<td>0.62</td>
</tr>
<tr>
<td>$A_4$</td>
<td>XOR/NOR/OR/AND_N</td>
<td>222</td>
<td>0.52</td>
</tr>
<tr>
<td>$R_1$</td>
<td>None (All Steps Enabled)</td>
<td>353</td>
<td>25.30</td>
</tr>
<tr>
<td>$R_2$</td>
<td>XOR/NOR</td>
<td>353</td>
<td>16.26</td>
</tr>
<tr>
<td>$R_3$</td>
<td>XOR/NOR/OR/AND_N</td>
<td>353</td>
<td>8.05</td>
</tr>
<tr>
<td>$R_4$</td>
<td>XOR/NOR/OR/AND_N</td>
<td>353</td>
<td>8.05</td>
</tr>
<tr>
<td>$Q$</td>
<td>None (Random)</td>
<td>21</td>
<td>$5.78 \times 10^{-3}$</td>
</tr>
</tbody>
</table>

6There are searches where removing steps can actually improve performance [4].
being hard wired into the process? The Avida paper reports that, in all case studies using stair step active information [16], "... at least one population evolved EQU."

What happens when no stair step active information is applied? “At the other extreme, 50 populations evolved in an environment where only EQU was rewarded, and no simpler function yielded energy. We expected that EQU would evolve much less often because selection would not preserve the simpler functions that provide foundations to build more complex features. Indeed, none of these populations evolved EQU, a highly significant difference from the fraction that did so in the reward-all environment.” [16]

Therefore, hard wired stair step active information is essential in order for Avida to produce results in reasonable time. We were able to do so in Section II-A2 only because the instruction set was diminished and the query count far exceeded the effort reported in the Avida paper [16].

6) Instruction Selection Active Information in the NAND Search: Some of the instructions in Table I are essential to do any of the logic functions in Table II. Performing any logic function without the (p) \texttt{nand} instruction, for example, would be difficult. Conversely, there are instructions in Table I that make little or no contributions to the goals of Avida. The instructions (j) \texttt{shift-r}, (k) \texttt{shift-l}, (I) \texttt{inc}, (m) \texttt{dec} (n) \texttt{add}, and (o) \texttt{sub}, for example, are typically deleterious to the performance of a logic operation.

The effects of removing various sets of instructions in Avida and the ratched evolutionary strategy searches are shown in Table IV. For both Avida and the ratchet search, removal of deleterious or otherwise nonessential instructions increases the active information significantly. Using only 14 of the 26 instructions in \( R_6 \) increases the NAIIPI with respect to the vanilla Avida in \( A_1 \) by a factor of over 300.

7) Initialization Active Information in the NAND Search: Another potential source of active information in search algorithms is initialization. If a search can be initialized in some sense closer to a solution, then we might expect faster convergence. Like any source of active information, the prior knowledge must be right. If we place the initialization farther from a solution, for example, convergence can take much longer.

In Avida, the instruction (c) \texttt{nop-C} plays an important role. The BX register is used as a default register and is used unless changed by a (a) \texttt{nop-A} or (c) \texttt{nop-C} instruction. In the default operation of \texttt{nand}, for example, the \texttt{nand} operation of registers BX and CX is performed and deposited in register BX. Storing intermediate values of Avida’s computation in register CX is therefore mandatory in computing the \texttt{nand} and can only be done using the (c) \texttt{nop-C} instruction. Apparently aware of this requirement, the designer of Avida set the initial value of all instructions not dedicated to organism replication to \texttt{nop-C}. The performance of Avida with this initialization is superior to initialization using nonmandatory \texttt{nop-A} or \texttt{nop-B} instructions. The performance of these and other initializations are summarized in Table V. In all cases, the NAIIPI deteriorates by at least a factor of a half, or as described in the figure caption, is DOA.

### III. CONCLUSIONS

#### A. Active Information

The Avida program uses numerous sources of active information to guide its performance to successful discovery of the EQU logic function. The sources include the following.

- **Stair step active information.** In the initial description of Avida, the authors write [16]

  “Some readers might suggest that we stacked the deck by studying the evolution of a complex feature that could be built on simpler functions that were also useful.”

This, indeed, is what the writers of Avida software do when using stair step active information. The importance of stair step active information is evident from the inabil-

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### TABLE IV

**Effects of Instruction Removal on Avida (A) and the Ratched Evolutionary Strategy Searches (R) for EQU.** The letters in the Instructions column correspond to the Letter Labeling of the Instructions in Table I. The entries for \( A_1 \) and \( R_6 \) in Table III are repeated here for ease of comparison. Ratched algorithms \( R_5 \) and \( R_6 \) used the same instructions as \( A_5 \) and \( A_6 \). All simulations have 353 runs. The values in the \( l_0/l_1 \) column should be multiplied by \( 10^{-5} \).

<table>
<thead>
<tr>
<th>Model</th>
<th>Instructions</th>
<th>Successes</th>
<th>( l_0/l_1 )</th>
</tr>
</thead>
<tbody>
<tr>
<td>( A_1 )</td>
<td>abc-defghi-jklmnopqrstuvwxyz</td>
<td>346</td>
<td>1.90</td>
</tr>
<tr>
<td>( A_5 )</td>
<td>abcdefghijklmnopqrstuvwxyz</td>
<td>353</td>
<td>5.16</td>
</tr>
<tr>
<td>( A_6 )</td>
<td>abc-defghi-jklmnopqrstuvwxyz</td>
<td>353</td>
<td>10.00</td>
</tr>
<tr>
<td>( R_1 )</td>
<td>abcdefghijklmnopqrstuvwxyz</td>
<td>353</td>
<td>25.30</td>
</tr>
<tr>
<td>( R_5 )</td>
<td>abc-defghi-jklmnopqrstuvwxyz</td>
<td>353</td>
<td>197.76</td>
</tr>
<tr>
<td>( R_6 )</td>
<td>abc-defghi-jklmnopqrstuvwxyz</td>
<td>353</td>
<td>593.80</td>
</tr>
</tbody>
</table>

### TABLE V

**Effects of Different Initializations on the Avida (A) for EQU.** The DOA designation indicates that nearly all organisms died before producing any offspring. In \( A_4 \), deaths were caused largely by Instruction Selection (V) \texttt{mov-head} and (W) \texttt{jmp-head}. These entries were removed in \( A_{11} \) and successes were achieved. A random selection of \texttt{nop-A}, \texttt{nop-B}, \texttt{nop-C} in Algorithm \( A_{10} \) also resulted in a DOA scenario. (The reason is as follows. As part of the Replication Loop, Avida searches for patterns of \texttt{nop}'s. In particular the end of the Avida program is marked by \texttt{nop-A}, \texttt{nop-B}. If that pattern appears anywhere else in the program it will be detected as the end of the program thus causing the replicator to fail.) With 85 randomly chosen \texttt{nop}'s, that pattern is almost certain to appear.) The entry for \( A_1 \) from Table III is repeated here for ease of comparison. All simulations have 353 runs. The values in the \( l_0/l_1 \) column should be multiplied by \( 10^{-5} \).

<table>
<thead>
<tr>
<th>Model</th>
<th>Initialization</th>
<th>Successes</th>
<th>( l_0/l_1 )</th>
</tr>
</thead>
<tbody>
<tr>
<td>( A_1 )</td>
<td>\texttt{nop-C}</td>
<td>346</td>
<td>1.90</td>
</tr>
<tr>
<td>( A_7 )</td>
<td>\texttt{nop-B}</td>
<td>318</td>
<td>0.81</td>
</tr>
<tr>
<td>( A_8 )</td>
<td>\texttt{nop-A}</td>
<td>324</td>
<td>0.85</td>
</tr>
<tr>
<td>( A_9 )</td>
<td>Random</td>
<td>DOA</td>
<td>DOA</td>
</tr>
<tr>
<td>( A_{10} )</td>
<td>Random-nops</td>
<td>DOA</td>
<td>DOA</td>
</tr>
<tr>
<td>( A_{11} )</td>
<td>Random w/o (u) and (v)</td>
<td>274</td>
<td>0.84</td>
</tr>
</tbody>
</table>
ity to generate a single EQU in Avida without using it [16].

- **Active information from Avida’s initialization.** The initialization in Avida recognizes the essential role of the nop-C instruction in finding the EQU. Initializing using all nonessential nop-A or nop-B instructions results in the a decrease in NAIPI in Avida.

- **Mutation, fitness, and choosing the fittest of a number of mutated offspring** [5] are additional sources of active information in Avida we have not explored in this paper.

### B. Disclosure

According to the principle of conservation of information [3], [4], [5], [7], [9], [10], [19], [20], [21], [22], [23], [28], [30], all computer search algorithms of moderate to high difficulty require active information.

The conservation of information principle in computer search, as manifest in the No Free Lunch Theorems, are [2]

- “... very useful, especially in light of some of the sometimes outrageous claims that had been made of specific optimization algorithms.”

To have integrity, computer simulations of evolutionary search like Avida should make explicit

- (1) a measure or assessment of the difficulty, \( I_D \), of the problem being solved,
- (2) the prior knowledge that gives rise to the active information in the search algorithm, and
- (3) a measure or assessment of the active information, e.g. either \( I_c \) or \( I_{B^*} \), introduced by the prior knowledge.

### REFERENCES


